

# Knobbe Martens Olson & Bear LLP

Intellectual Property Law



09/037,945

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December 30, 2005

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313, on

December 30, 2005

(Date)

Adeel S. Akhtar, Reg. No. 41,394

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: **Certificate of Correction**

Title: STREAMLINED FIELD ISOLATION PROCESS  
Letters Patent No. 6,835,634 B1  
Issued: December 28, 2004  
Our Reference: MICRON.003C1

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP

Adeel S. Akhtar  
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Enclosures

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UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 6,835,634

DATED : December 28, 2004

INVENTOR(S): Fazan et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On page 2, at column 1, line 1 under "Other Publications" please replace "Microchipn" with --  
Microchip --.

At column 1, line 52, after reducing please delete ",,.

At column 3, line 54, please replace "crow" with -- grow --.

At column 4, line 40, after "(solid)+" please replace "O<sub>2</sub>" with -- 3O<sub>2</sub> --.

In Claim 2, at column 5, line 21, after "claim 1" please insert -- , --.

In Claim 2, at column 5, lines 21-22, please delete "comprises substantially only" and insert -- consists  
essentially of --.

In Claim 3, at column 5, line 23, please delete "further comprising" and insert -- wherein forming the  
field isolation region comprises --.

In Claim 3, at column 5, line 24, please delete "wafer" and insert -- substrate --.

In Claim 4, at column 5, line 26, please delete "claim 1, further comprising" and insert -- claims 3,  
wherein forming the field isolation region comprises --.

In Claim 4, at column 5, line 27, please delete "semiconducting wafer" and insert -- semiconductor  
substrate --.

In Claim 6, at column 5, line 38, please delete "200° C." and insert -- 900° C. --.

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PATENT NO. 6,835,634

\* December 14, 2005

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